

# MOS FIELD EFFECT TRANSISTOR $\mu$ PA675T

# N-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR HIGH SPEED SWITCHING

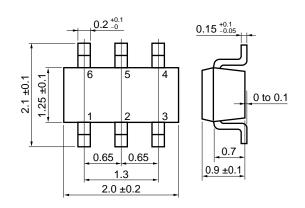
## DESCRIPTION

The  $\mu$  PA675T is an N-channel vertical MOS FET. Because it can be driven by a voltage as low as 1.5 V and it is not necessary to consider a drive current, this FET is ideal as an actuator for low-current portable systems such as headphone stereos and video cameras.

#### **FEATURES**

- Two MOS FET circuits in package the same size as SC-70
- · Automatic mounting supported
- Gate can be driven by a 1.5 V power source
- Because of its high input impedance, there's no need to consider a drive current
- Since bias resistance can be omitted, the number of components required can be reduced

# PACKAGE DRAWING (Unit: mm)



#### **PIN CONNECTION**

### **ORDERING INFORMATION**

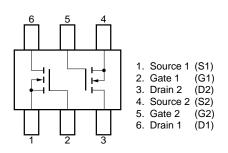
PART NUMBER	PACKAGE
$\mu$ PA675T <sup>Note</sup>	SC-88 (SSP)

Note Marking: SA

### ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Vgs = 0 V)	Vdss	16	V
Gate to Source Voltage (VDS = 0 V)	Vgss	±7.0	V
Drain Current (DC) (Tc = 25°C)	ID(DC)	±0.1	А
Drain Current (pulse) <sup>Note</sup>	D(pulse)	±0.2	А
Total Power Dissipation (Tc = 25°C)	Pτ	0.2	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	–55 to +150	°C

Note  $PW \le 10 \text{ ms}$ , Duty Cycle  $\le 50\%$ 



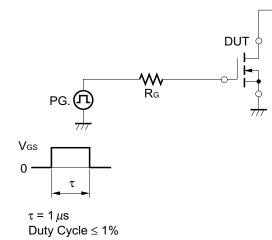
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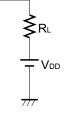
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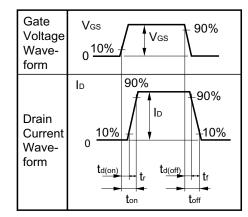
# ELECTRICAL CHARACTERISTICS (TA = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	Vds = 16 V, Vgs = 0 V			1.0	μA
Gate Leakage Current	lgss	$V_{GS} = \pm 7.0 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$			±3.0	μA
Gate Cut-off Voltage	VGS(off)	$V_{DS} = 3 V$ , $I_D = 10 \mu A$	0.5	0.8	1.1	V
Forward Transfer Admittance	y <sub>fs</sub>	V <sub>DS</sub> = 3 V, I <sub>D</sub> = 10 mA	20			mS
Drain to Source On-state Resistance	RDS(on)1	Vgs = 1.5 V, I <sub>D</sub> = 1 mA		20	50	Ω
	RDS(on)2	$V_{GS} = 2.5 \text{ V}, I_D = 10 \text{ mA}$		7	15	Ω
	RDS(on)3	$V_{GS} = 4.0 \text{ V}, I_D = 10 \text{ mA}$		5	12	Ω
Input Capacitance	Ciss	Vds = 3 V		10		pF
Output Capacitance	Coss	V <sub>GS</sub> = 0 V		13		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		3		pF
Turn-on Delay Time	td(on)	V <sub>DD</sub> = 3 V, I <sub>D</sub> = 10 mA		15		ns
Rise Time	tr	Vgs = 3 V		70		ns
Turn-off Delay Time	td(off)	R <sub>G</sub> = 10 Ω		100		ns
Fall Time	tr			110		ns

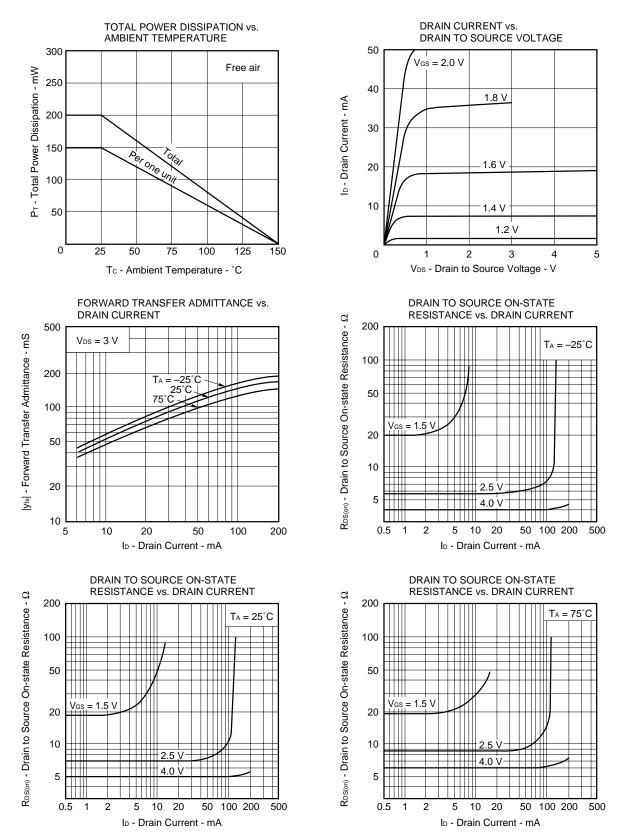
#### SWITCHING TIME MEASUREMENT CIRCUIT AND CONDITIONS



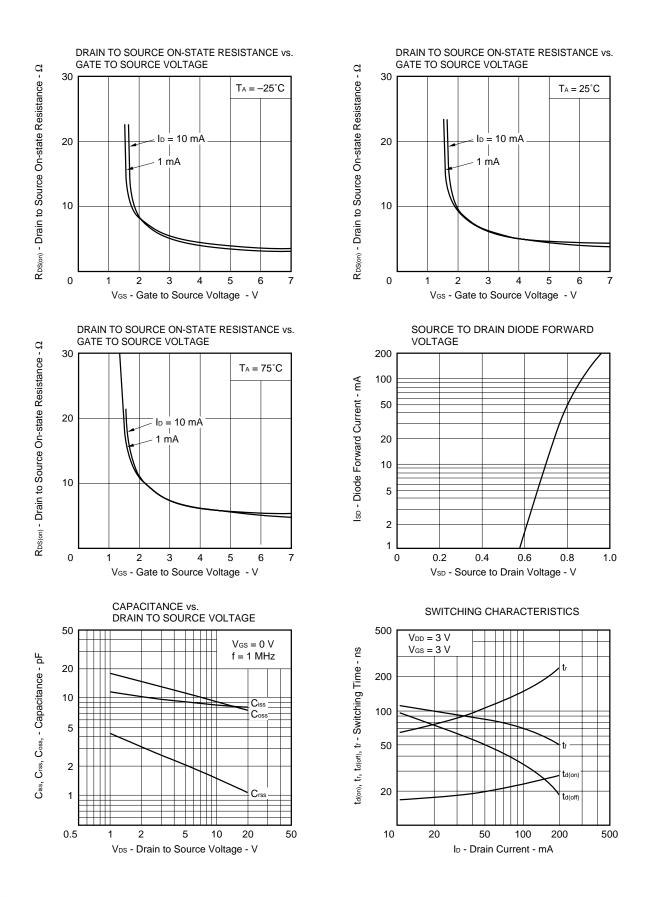




#### TYPICAL CHARACTERISTICS ( $T_A = 25^{\circ}C$ )



Data Sheet G15454EJ1V0DS



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